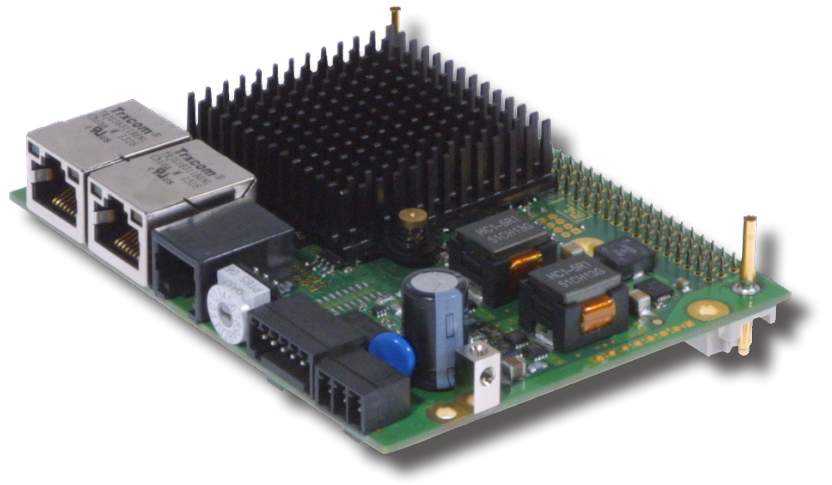


COP-MAS2

Compact Peripherals Master



- ➔ GinLink and COP master
- ➔ ARM Cortex A9 single/dual-core 800 MHz
- ➔ 256 MB DDR3 RAM

Facts	
Interfaces	GinLink slave / GinLink master Gigabit ethernet RS232 COP bus
CPU	ARM Cortex A9 single-core 800 MHz / ARM Cortex A9 dual-core 800 MHz *
Cache	L1: 32/32K KByte per core L2: 512 KByte
Memory	256 MByte DDR3-800, 400 MHz, 32 Bit
Non-Volatile Memory	8 MByte flash 512 KB NVRAM* MicroSD card slot*
Motion-Control	Max. 32 / 64 axes Max. 16 kHz position loop
Operating system	Indel real-time OS (INOS)
Abmessungen	100 x 80 mm (w x d)
COP case requirement	1 COP slot

* Not for option LITE

The COP-MAS2 is the ideally suited CPU board for applications of basic to medium complexity.

The compact design and its high performance ARM CPU make the COP-MAS2 applicable for many different use cases. The COP-MAS2 can control up to six COP modules directly via the local bus connector. Therefore it is possible to implement a compact control system within a single COP node.

Additionally it is possible to use the COP-MAS2 as GinLink master or slave.