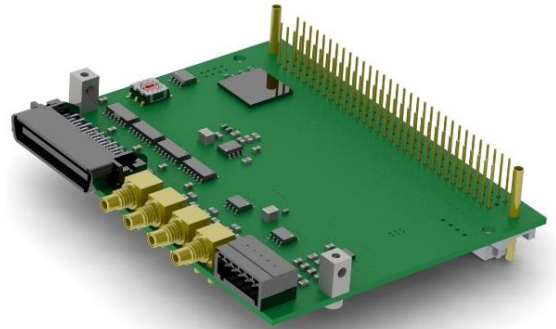


26. COP-LVDS (Low Voltage Differential Signaling Interface)

COP-LVDS 611144500

The COP-LVDS module has been developed specially developed for the integration of Lightning II Servo Drivers via the GS bus from Cambridge Technology. The GS bus is a serial bus protocol and runs over a 40-pin flat-ribbon cable (M-LVDS). 8 channels of 32 bits with a clock frequency of 24MHz are transmitted. This results in a sampling rate of 93.75kHz per axis.



26.1. Technical Specifications

LVDS		
Number of interfaces	1	
Data rate	24	MHz
Data bits	32	Bit
Channels	8	
Sampling rate	93.75	kHz
Maximum cable length	2	m
Modul		
Maximum power consumption at 24V node power supply	200	mA

26.2. Pin Assignment

X1					
Nr	Dir	Bez	Bez	Dir	Nr
1		GND	GND		21
2		+CMD_Clk	-CMD_Clk		22
3		GND	GND		23
4		+CMD_FS	-CMD_FS		24
5		GND	GND		25
6		+CMD_D	-CMD_D		26
7		GND	GND		27
8		+Clk_En	-Clk_En		28
9		GND	GND		29
10					30
11					31
12		GND	GND		32
13		+COM	-COM		33
14		GND	GND		34
15		+Stat_D	-Stat_D		35
16		GND	GND		36
17		+Stat_FS	-Stat_FS		37
18		GND	GND		38
19		+Stat_Clk	-Stat_Clk		39
20		GND	GND		40

X2		
Nr	Dir	Bez
1		+5V
2		Inp-0
3		Inp-1
4		Inp-2
5		Inp-3
6		GND

26.3. Available Options

Item Number	Label	Option	Description
611144500	COP-LVDS		LVDS Interface Sampling rate: 93.75kHz